

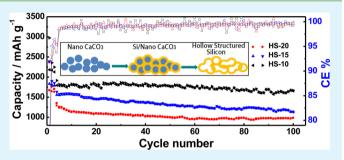
Hollow Structured Silicon Anodes with Stabilized Solid Electrolyte Interphase Film for Lithium-Ion Batteries

Qiuliang Lv, Yuan Liu, Tianyi Ma, Wentao Zhu, and Xinping Qiu*

Key Laboratory of Organic Optoelectronics and Molecular Engineering, Department of Chemistry, Tsinghua University, Beijing 100084, China

Supporting Information

ABSTRACT: Silicon has been considered as a promising anode material for the next generation of lithium-ion batteries due to its high specific capacity. Its huge volume expansion during the alloying reaction with lithium spoils the stability of the interface between electrode and electrolyte, resulting in capacity degradation. Herein, we synthesized a novel hollow structured silicon material with interior space for accumulating the volume change during the lithiation. The as-prepared material shows excellent cycling stability, with a reversible capacity of ~1650 m Ah g⁻¹ after 100 cycles, corresponding to 92% retention. The electrochemical impedance spectroscopy



and differential scanning calorimetry were carried out to monitor the growth of SEI film, and the results confirm the stable solid electrolyte interphase film on the surface of hollow structured silicon.

KEYWORDS: anode, silicon, Coulombic efficiency, interface stability, lithium-ion battery

■ INTRODUCTION

Silicon has been regarded as a promising anode material for the next generation of lithium-ion batteries $(LIBs)^{1-4}$ due to its low discharge potential and high specific capacity. However, huge volume expansion during the alloying reaction with lithium causes the pulverization of particles, disintegration of electrodes, 5-7 and rupture of the existing interface layer, which promotes the continual formation of SEI film. Excessive growth of SEI film, observed in previous work, 8,9 results from the increase of impedance, irreversible capacity loss, and low Coulombic efficiency during cycle. 10-12 The anode with low Coulombic efficiency continuously consumes lithium ions from the cathode and causes rapid capacity degradation of the balanced full battery.

Nanostructured silicon materials, such as hollow nanospheres, ^{13–15} silicon core and hollow carbon shell nano-composites, ^{16–19} and 1D hollow silicon nanotubes, ^{20–23} with void inside have been reported to have improved cycle performance. Voids in the materials can efficiently accommodate the large strain from the lithiation and dethiathtion reaction. The nanostructure can also absorb the electrolyte and shorten the diffusion paths of lithium ions, which are beneficial for rate capability. In spite of the enhanced electrochemical performance, the direct and effective characterization of SEI film stability is still absent of evidence; in addition, the vital process of engineering the voids in these materials is not very efficient. In this work, we report a facile approach to preparing hollow structured silicon material through a chemical vapor deposition (CVD) method with calcium carbonate (CaCO₃) as the template. With the built-in space, this product presents a

remarkable capacity retention and fairly high Coulombic efficiency. The stablity of SEI film is demonstrated as well.

EXPERIMENTAL SECTION

Preparation of Hollow Structured Silicon Material. Commercial nanostructured CaCO3 (ShanXi NanoMaterials Technology Co., Ltd.) was directly used as the template in CVD process. Silicon deposition was conducted in a horizontal tube furnace at 480 °C with mixed gas of 5 wt % SiH₄ and Ar as silicon source. The wall thickness of the silicon form was controlled by the time of deposition and the samples denoted as HS-10, HS-15, and HS-20 corresponding to the deposition time of 1, 1.5, and 2 h, respectively. The CaCO3 template was removed with hydrochloric acid (5 wt %). SiO_x on the surface of hollow structured silicon was removed in hydrofluoric acid (10 wt %) solution.

Structural Characterization. Morphologies of samples were observed on a transmission electron microscopy (TEM, JEOL model JEM-2011) operating at 80 keV and a scanning electron microscopy (SEM, HITACHI S-5500) operating at 5 keV. X-ray diffraction (XRD) spectra were collected using an X-ray diffractometer (Bruker D8 Advance) with a Cu Klpha radiation source, and values of 2 hetawere scanned in the range of 10° to 90° at a rate of 8° min⁻¹ with a step of 0.02°. X-ray photoelectron spectroscopy (XPS) measurements were conducted with a PHI Quantera SXM spectrometer using a focused and monochromatized Al K α radiation, and the binding energy scale was calibrated using the C 1s peak at 284.8 eV. N₂ sorption measurements were carried out on a Quantachrome NOVA 1000e at 77.3 K. Specific surface area and pore-size distribution were

Received: July 3, 2015 Accepted: September 24, 2015 Published: September 24, 2015 calculated using the Brunauer–Emmett–Teller (BET) and Barrett–Joyner–Halenda (BJH) methods, respectively. The differential scanning calorimetry (DSC) was performed on a Mettler-Toleda 821 calorimeter. The Li_xSi material was scratched from the electrode in the argon glovebox and sealed in a hermetic high-pressure stainless steel gold-plated crucible (Mettler Toledo). DSC measurements were performed from 30 to 300 $^{\circ}$ C at a temperature ramp of 2 $^{\circ}$ C min $^{-1}$.

Electrochemical Measurements. The electrodes for electrochemical tests were prepared by coating slurries (containing 80 wt % active material, 10 wt % carbon black (Super P Li, TIMCAL), and 10 wt % poly(acrylic acid) (PAA)) onto copper foil. The loading of active materials in the electrode is 0.4-0.6 mg cm⁻². 2025 coin cells were assembled with lithium foil as the counter electrode. The electrolyte solution was composed of 1 M LiPF₆ dissolved in a nonaqueous mixture of ethylene carbonate/dimethyl carbonate/ethylmethyl carbonate (EC/DMC/EMC, 1:1:1 volume ratio) with 2.5 wt % vinylene carbonate (VC) as an additive (Zhangjiagang Guotai-Huarong New Chemical Materials Co., Ltd.). Galvanostatic tests were performed between 0.05 and 1.5 V versus Li/Li⁺ with a relaxation period of 1 min at the end of each charge and discharge process. A total of three electrode cells (ECC-REF model, EL-CELL) were assembled for electrochemical impedance spectra (EIS) tests using Li foil as the reference and counter electrodes. EIS measurements were conducted on a PARSTAT2273 electrochemical workstation (AME-TEK). The AC potential was controlled with an amplitude of 5.0 mV in the frequency range of 10⁵-0.02 Hz. Before the tests, a constant current density of 100 mA g⁻¹ was applied in the discharge process until the voltage reached 1 mV versus Li/Li+, and then the cells remained at an open-circuit state for 2 h to stabilize their potential. All cells were assembled in an Ar-filled glovebox with moisture and oxygen contents below 1 ppm.

■ RESULTS AND DISCUSSION

We fabricated hollow structured silicon with a simple template method, as illustrated in Figure 1. The silicon was deposited

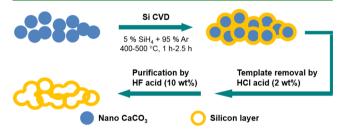


Figure 1. Schematic diagram of the fabrication process of porous silicon

onto a nanosized CaCO₃ template by a CVD process. The CaCO₃ template was washed away in hydrochloric acid solution. The resulting silicon was then treated in hydrofluoric acid to remove the SiO_x. The morphology of nanosized CaCO₃ particles is nearly spherical in shape, as shown in panels a and b of Figure 2, with a narrowly distributed particle size of 50 nm. After deposition, a layer of silicon can be observed on the surface of nanosized CaCO₃ (Figure 2c,d). An interconnected hollow structured silicon material was left after the removal of the template in hydrochloric acid (Figure 2e,f).

The wall thickness of porous silicon can be tailored by the control of time of CVD in a fixed flow rate of the precursor gas. TEM images in Figure 3 show that the wall thickness in the hollow structured silicon increases with the deposition time. Broad diffuse rings in the selected-area electron diffraction (SAED) pattern of the HS-10 sample (inset of Figure 3a) reveal that silicon is amorphous. In the XRD patterns of all samples, as shown in Figure 3d, characteristic peaks of crystalline silicon

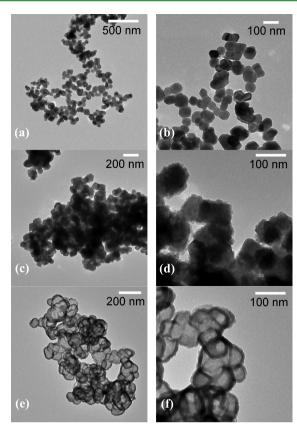


Figure 2. Transmission electron microscopy images of (a,b) nano CaCO₃, (c,d) CaCO₃ deposited with silicon layer, and (e,f) hollow structured silicon.

(PDF#65-1060) around 28°, 47°, and 56° are absent, indicating the amorphous structure of silicon, in agreement with the results of SAED. The XPS spectra of Si 2p in Figure 3e are fitted well using a nonlinear Shirley-type background²⁴ and a combination of 80% Gaussian and 20% Lorentzian line shapes. The first main 3/2-1/2 doublet (the spin-orbit splitting is 0.6 eV and the intensity ratio is 3:1), located at 99.1-99.7 eV, corresponds to Si⁰ (75% content). The component located at a higher binding energy (100.0 eV) is associated with SiO_x formed on the surface, with a proportion of 25%.²⁵ The nitrogen adsorption and desorption isotherms in Figure S1a show a sharp capillary condensation step at high relative pressures $(P/P_0 = 0.85-0.99)$, indicating the existence of large pores. Corresponding pore size distributes mainly in the range of 20 to 100 nm (Figure S1b), which is attributed to the removal of the site-occupying CaCO3 template. With the increase of silicon wall thickness, the specific surface area and pore volume decrease obviously (Table S1) owing to the integrated silicon layer and increased density of material.

Galvanostatic tests were performed at a constant current densities of 100 mA $\rm g^{-1}$ for the first three cycles and 500 mA $\rm g^{-1}$ for later cycles. The specific capacity was calculated based on the mass of silicon. Figure 4a shows the first and second charge and discharge profiles of HS-10 at a rate of 100 mA/g. A long platform is observed in the first discharge process and disappeared in subsequent cycles, corresponding to the SEI film formation. The sloping voltage profile suggests a typical lithiation reaction of amorphous silicon. For the first discharge-and-charge cycle, the capacities reach 2973 and 2367 mAh $\rm g^{-1}$, respectively, corresponding to the Coulombic

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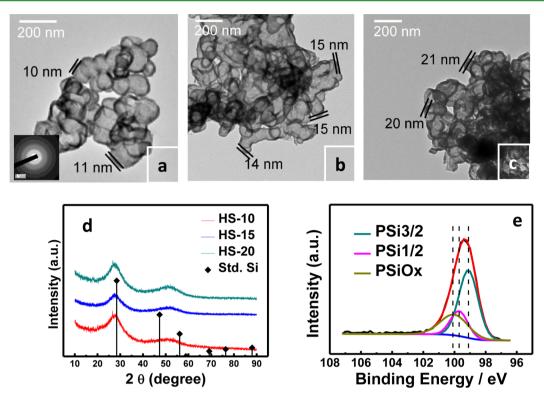


Figure 3. TEM images of hollow structured silicon with different thicknesses: (a) HS-10 (inset is the corresponding selected-area electron diffraction pattern), (b) HS-15, (c) HS-20, (d) X-ray power diffraction patterns of HS samples, and (e) X-ray photoelectron spectroscopy analysis of Si 2p of HS-10.

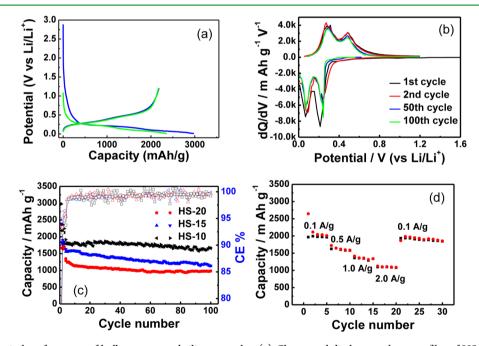
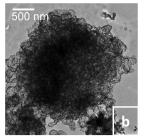


Figure 4. Electrochemical performance of hollow structured silicon samples. (a) Charge and discharge voltage profiles of HS-10 at a 0.1A/g rate in the first two cycles. (b) Differential capacity of HS-10 in different cycles. (c) Capacity retention during 100 cycles for HS-10, HS-15, and HS-20. (d) Capacity of HS-10 at various charge and discharge rates.

efficiency of 73.4%. Plots of the differential capacity of HS-10 in different cycles are presented in Figure 3b. The peak at 0.2 V in the first discharge suggests the lithiation process in amorphous silicon. During the first charge, two peaks at 0.32 and 0.48 V indicate the formation of amorphous silicon during the dealloying reaction, this being consistent with the previous report in the literature for amorphous silicon electrodes. ^{14,28–30}

Peak values from 50 to 100 cycles are almost unchanged, indicating little capacity losses during cycling. 15

Figure 4c illustrates the cycle performance and Coulombic efficiency. Among these samples, HS-10 shows the highest capacity retention (92%) after 100 cycles, corresponding to the reversible capacity of 1654 mAh $\rm g^{-1}$. The benefit from the short lithium-ion diffusion distance in this nanostructure, excellent



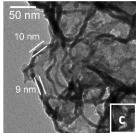


Figure 5. (a) SEM image of HS-10 electrode after 100 cycles and (b,c) TEM images of HS-10 after 100 cycles, where SEI film was washed away.

rate capability, is also demonstrated (Figure 4d). After an increase in the current density from 0.1 to 2 A g⁻¹, the specific capacity of HS-10 becomes more than 1000 m Ah g⁻¹; when the current density changes back to 0.1 A g⁻¹, 98.5% of the capacity of the first cycle is recoverable. The increase of the wall thickness of hollowed silicon decreases the capacity retention obviously (76% for HF-15 and 73% for HS-20 after 100 cycles); this is possible due to the limited interior space for volume change.

To investigate the evolution process of SEI during cycles, we took the HS-10 electrode out of the coin cell after 100 cycles and washed off the electrolyte residue in dimethyl carbonate. Primary hollow structured silicon covered with SEI film can be observed in Figure 5a, and the surface of the hollow structured silicon and the carbon black additives can be clearly distinguished, demonstrating no excessive growth of SEI film. We used 1 mM acetic acid solution to remove the SEI film. The shape (Figure 5b) and the wall (Figure 5c) were maintained without fractures and cracks. This indicates that hollow structured silicon can withstand the volume change.

Electrochemical impedance spectroscopy (EIS) measurements were further conducted. Typical Nyquist plots of hollow structured silicon electrode are illustrated in Figure 6a. The semicircle in the midfrequency regime is generally associated with interfacial phenomena, either the charge transfer or the occurrence of SEI film between the active particles and the liquid electrolyte;¹¹ the impedance tail in the low-frequency region can be attributed to the bulk diffusional effects. R_{Sur} here

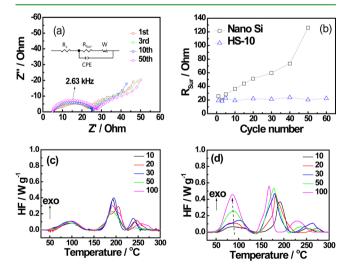


Figure 6. (a) Nyquist plot of HS-10 at the end of discharge after different cycles (inset is the equivalent circuit). (b) The evolution of the resistance of the semicircle. (c,d) DSC curves for HS-10 and nanosized Si after different cycles.

is used to evaluate the whole surface resistance (the equivalent circuit is shown in the inset of Figure 6a). In Figure 6a and 6b, $R_{\rm Sur}$ maintains around 20 Ohm for HS-10, which indicates the stable SEI during cycling. After five cycles, the constancy of the characteristic frequency (2.63 kHz) suggests that the kinetics of the charge-transfer reaction and the SEI film conductivity do not change. By contrast, the continuous increase of $R_{\rm Sur}$ is observed for the electrode with Si nanoparticles in our previous research, 32 indicating excessive SEI growth.

To evaluate the amount of SEI film directly, we developed a method based on differential scanning calorimetry (DSC). In detail, the cells were discharged at a constant current density of 100 mA g⁻¹ to the voltage of 1 mV and remained at an opencircuit state for 2 h to stabilize their potential. They were then carefully opened in the glovebox, and the retrieved electrodes were rinsed in dimethyl carbonate solvent to remove the electrolyte and then dried in a vacuum overnight. LixSi material was scratched from the electrode and was sealed into a hermetic high-pressure stainless-steel crucible for DSC tests. Figure 6c,d present the DSC curves for lithiated silicon after different cycles. An exothermic signal at 86-100 °C is visible for all the curves of HS-10 and nano Si material. By analogy with the lithiated graphite and nanosized silicon electrode,³³ it can be reasonably suggested that these exothermic peaks are attributed to the transformation of metastable SEI layer components. At elevated temperatures, the metastable components in SEI film (ROCO2Li compounds, ROLi, polycarbonate species, and lithium oxalate)33,34 decompose and react with lithium in Li_xSi.

For nanosized Si, much larger exothermic heat at $86-100~^{\circ}\text{C}$ is observed, indicating that the amount of SEI film increases continuously. Meanwhile, there is no obvious change of the exothermic peak for HS-10, which suggests that SEI formation in hollow structured silicon is confined and that the interface layer between electrode and electrolyte is more stable. Another remarkable phenomenon is that the peak position at higher temperatures (167–190 °C) shows a shift to low temperatures for nanosized Si material. According to previous literature, 36-38 this peak can be associated with the conversion of metastable α -Li, Si phases to thermodynamically stable ones, where lithium content (x value) is account for the shape and position of the peak. The continuous formation of SEI film on nanosized Si consumes more lithium in the reaction, resulting in a decreased x value in α -Li_xSi and a further shift of the peak position.^{37,38} In HS-10, exothermic peaks of α -Li_xSi conversion maintain a similar shape and position, which confirm that the interface stability in hollow structured silicon material is much better.

In summary, hollow structured silicon material is synthesized using a facile chemical-vapor deposition method in combination with nanosized calcium carbonate as a template. The influence of silicon shell thickness on electrochemical performance have been discussed. This material exhibits excellent cycling stability and rate performance without crushing the hollow structure. The Coulombic efficiency during cycling is maintained at nearly 100%, which is beneficial for a long cycle life. Furthermore, EIS and DSC observations on the SEI film transformation lead us to a deeper understanding of the interface stability. Considering the fact that nanosized calcium carbonate is commercially available,³⁹ this hollow structured silicon material is a promising anode candidate for the next generation of lithium-ion batteries.

ASSOCIATED CONTENT

S Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acsami.5b05970.

Image showing the nitrogen sorption isotherms of silicon foam samples and corresponding pore-size distributions and a table showing the specific surface area and pore volume of HS samples. (PDF)

AUTHOR INFORMATION

Corresponding Author

*E-mail: qiuxp@mail.tsinghua.edu.cn; tel: +86-10-62794234; fax: +86-10-62794234.

Notes

The authors declare no competing financial interest.

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